

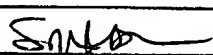
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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Application Number		10/645,861-7697	
				Filing Date		8/20/2003	
				First Named Inventor:		Wingyu Leung	
				Examiner: Stephen M. Baker		GROUP: 2133	
Sheet	1	of	5	Attorney Docket Number		MST-012-1P	
U.S. PATENT DOCUMENTS							
Examiner Initials*	Cited No. ¹	Document Number Number - Kind Code ^{2,4(2name)}		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Passages or Figures Appear
SM	F01	US-3,814,921		06/04/1974	Nibby et al.		
SM	F02	US-3,969,706		07/13/1976	Proebsting et al.		
SM	F03	US-4,045,783		08/30/1977	Harland		
SM	F04	US-4,389,714		06/21/1983	Nakao		
SM	F05	US-4,768,197		08/30/1988	Petolino et al.		
SM	F06	US-4,839,868		06/13/1989	Sato et al.		
SM	F07	US-4,884,271		11/28/1989	Concha et al.		
SM	F08	US-4,933,904		06/12/1990	Stewart et al.		
SM	F09	US-5,008,886		04/16/1991	Chinnaswamy et al.		
SM	F10	US-5,235,693		08/10/1993	Chinnaswamy et al.		
SM	F11	US-5,241,503		08/31/1993	Cheng		
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER SIGNATURE: <u>SP/LR</u>				DATE CONSIDERED: <u>3/5/07</u>			

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SM	F12	US-5,289,584		02/22/1994	Thome et al.			
SM	F13	US-5,369,651		11/29/1994	Marisetty			
SM	F14	US-5,402,389		03/28/1995	Flannagan et al.			
SM	F15	US-5,430,742		07/04/1995	Jeddeloh et al.			
SM	F16	US-5,469,558		11/21/1995	Lieberman et al.			
SM	F17	US-5,502,835		03/26/1996	Le et al.			
SM	F18	US-5,513,148		04/30/1996	Zagar			
SM	F19	US-5,519,839		05/21/1996	Culley et al.			
SM	F20	US-5,519,847		05/21/1996	Fandrich et al.			
SM	F21	US-5,579,267		11/26/1996	Koshikawa			
SM	F22	US-5,586,282		12/17/1996	Iino et al.			
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SM	F23	US-5,592,435		01/07/1997	Mills et al.		
SM	F24	US-5,615,169		03/25/1997	Leung		
SM	F25	US-5,671,187		09/23/1997	Childers et al.		
SM	F26	US-5,687,353		11/11/1997	Chen et al.		
SM	F27	US-5,708,624		01/13/1998	Leung		
SM	F28	US-5,778,237		07/07/1998	Yamamoto et al.		
SM	F29	US-5,829,026		10/27/1998	Leung et al.		
SM	F30	US-6,020,773		02/01/2000	Kan et al.		
SM	F31	US-6,052,011		04/18/2000	Dasgupta		
SM	F32	US-6,701,480	B1	03/02/2004	Karpuska et al.		
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SD	F34	US-6,854,059	B2	02/08/2005	Gardner			
SD	F35	US-7,064,993	B2	06/20/2006	Gyohten et al.			
SD	F36	US-7,099,216	B2	08/29/2006	Luk et al.			
SD	F37	US-2002/0029365	A1	03/07/2002	Sato et al.			
SD	F38	US-2004/0039883	A1	02/26/2004	LaBerge et al.			
SD	F39	US-2004/0044943	A1	03/04/2004	Jacquet et al.			
SD	F40	US-2004/0064646	A1	04/01/2004	Emerson et al.			
SD	F41	US-2004/0172504	A1	09/02/2004	Balazich et al.			
SD	F42	US-2004/0213064	A1	10/28/2004	Gyohten et al.			
SD	F43	US-2005/0185492	A1	08/25/2005	Harrand			
SD	F44	US-2006/0034132	A1	02/16/2006	Jain			
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Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T ⁶
		Cntry Code ³	Number ⁴	Kind Code ^{5(f)(known)}				
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SM	F45	US-2006/0080589	A1	04/13/2006	Holm et al.		
SM	F46	US-2006/0156196	A1	07/13/2006	Takahashi et al.		
SM	F47	US-2006/0158950	A1	07/20/2006	Klein		
SM	F48	US-2006/0200723	A1	09/07/2006	Carnevale et al.		
SM	F49	US-2006/0200728	A1	09/07/2006	Nagai et al.		
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SM	Bondurant, D. "15nsec Enhanced DRAM Upgrades System Performance in 72-Pin SIMM Socket", Silicon Valley Personal Computer Design Conference Proceedings, Date Unknown, pp. 81-86						
SM	Dosaka et al. "A 100-Mhz 4-Mb Cache DRAM with Fast Copy-Back Scheme", IEEE Journal of Solid-State Circuits, Vol. 27, No. 11, Nov. 1992, pp. 1534-1539						
SM	Sunaga et al. "A Full Bit Prefetch Architecture for Synchronous DRAM's", IEEE Journal of Solid-State Circuits, Vol. 30, No. 9, Sep. 1995, pp. 998-1005						
SM	Pentium Processor 3.3V Pipelined BSRAM Specification, Ver. 2.0, Intel Corp., 25 May 1995, pp. 1-44						
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